

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---------------------------------------|---|------------------|---------|------------------|
| L1 | 81 | (stress adj3 test\$3) same flash\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:10 |
| L2 | 876 | (stress adj3 test\$3) and flash\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:11 |
| L3 | 54 | L2 and (select\$4 adj3 transistor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:11 |
| L4 | 54 | L2 and (select\$3 adj3 transistor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:11 |
| L5 | 3 | L4 and activat\$4 and de\$1activat\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:11 |
| L6 | 606 | flash and pre\$1decoder\$2 and buffer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |
| L7 | 107 | L6 and shifter\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |
| L8 | 107 | L7 and decoder\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |

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| L9 | 88 | L8 and (select\$3 adj3 signal) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |
| L10 | 4 | L9 and (stress\$3 adj2 test\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |
| L11 | 9687 | 365/185.33,189.05,189.11,201, 230.08.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |
| L12 | 67 | column with transistor with stress\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |
| L13 | 33 | L12 and L11 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:15 |
| L15 | 29 | L13 and @ad<"20021212" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:15 |
| L16 | 14 | L15 and buffer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:15 |
| L17 | 3 | L16 and (shift\$5 or shifter\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:15 |

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| L18 | 0 | ((stress adj2 test) and (flash\$2 adj2 memory) and (column adj2 select\$3) and (pre\$1determin\$2 adj3 bit adj3 line) and ((turn adj2 on) or activat\$3 or activ\$2) and (voltage or power or suppl\$3 or potential) and external and ((turn adj2 off) or de\$1activat\$3 or de\$1activ\$2) and (address or addreabl\$3)).clm. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:22 |
| L19 | 0 | (flash and pre\$1decoder\$1 and (pre\$1determin\$2 adj3 bit adj3 line) and buffer and decoder\$2 and (column adj2 address) and shifter\$2 and (voltage or power or potential or supply) and (PMOS near3 transistor) and (NMOS near3 transistor)).clm. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:27 |
| L20 | 0 | (flash and pre\$1decoder\$1 and (pre\$1determin\$2 adj3 bit adj3 line) and buffer and decoder\$2 and (column adj2 address) and shifter\$2 and (voltage or power or potential or supply) and (column adj3 decoder\$2) and (stage adj2 column) and (column adj5 selection adj5 signal) and (data adj3 line)).clm. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:29 |
| L21 | 8 | jeong-jae.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:29 |
| L22 | 0 | lim-heung.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:29 |
| L24 | 13 | lim-heung-\$in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:29 |

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| L25 | 0 | jeong-jae-y.in. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:30 |
| L27 | 8 | 11 and 2 and 6 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:31 |
| S1 | 1318 | 365/185.33.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:19 |
| S2 | 3321 | 365/201.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:20 |
| S3 | 1680 | 365/230.08.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:21 |
| S4 | 2880 | 365/189.05.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:21 |
| S5 | 1163 | 365/189.11.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:22 |
| S6 | 9339 | 365/185.33,189.05,189.11,201, 230.08.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:24 |

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| S7 | 78 | (stress adj3 test\$3) same flash\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:10 |
| S8 | 9 | S7 and S6 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:25 |
| S9 | 6 | S7 and S2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:25 |
| S10 | 803 | (stress adj3 test\$3) and flash\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:26 |
| S11 | 39 | S10 and column and (select\$3 adj3 transistor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:27 |
| S12 | 50 | S10 and (select\$3 adj3 transistor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:11 |
| S13 | 2 | S12 and activat\$3 and de\$1activat\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:11 |
| S14 | 55 | S10 and S6 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:29 |

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| S15 | 570 | flash and pre\$1decoder\$2 and buffer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:31 |
| S16 | 100 | S15 and shifter\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:31 |
| S17 | 100 | S16 and decoder\$2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:32 |
| S18 | 81 | S17 and (select\$3 adj3 signal) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:32 |
| S19 | 3 | S18 and (stress\$3 adj3 test\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |
| S20 | 10 | S18 and stress\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:34 |
| S21 | 18 | (stress adj3 test\$3) and pre\$1decoder and (column adj3 selection) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:39 |
| S22 | 16 | S21 and cell | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:39 |

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| S23 | 16 | S22 and signal | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 01:42 |
| S24 | 63 | column with transistor with stress\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 02:43 |
| S25 | 32 | S24 and S6 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 02:43 |
| S26 | 29 | S25 and @ad<"20021212" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/12 02:44 |
| S27 | 14 | S26 and buffer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:13 |
| S28 | 3 | S27 and (shift\$5 or shifter\$2) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/18 09:15 |